EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER

09266359

PUBLICATION DATE

07-10-97

APPLICATION DATE

28-03-96

APPLICATION NUMBER

08073569

APPLICANT: TAIYO YUDEN CO LTD;

INVENTOR:

NAKAJIMA MITSUO;

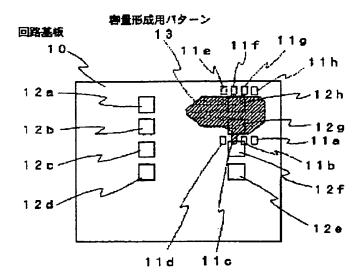
INT.CL.

H05K 1/02 H03F 3/38

TITLE

CIRCUIT PATTERN AND ISOLATED

AMPLIFIER USING THE PATTERN



ABSTRACT :

PROBLEM TO BE SOLVED: To provide a circuit pattern for an isolated amplifier which has an enhanced common mode voltage rejection ratio and also to provide an isolated amplifier using the circuit pattern.

SOLUTION: In the isolated amplifier using a circuit board 10, a circuit pattern 13 for capacity formation is connected to a land 12c for a grounding input terminal of an operational amplifier 3 and forms a predetermined electrostatic capacity between primary side lands 11a to 11d of a photocoupler. Thereby a common mode signal input to the primary side of the photocoupler is input to a non-inversion input terminal of the operational amplifier 3 through a stray capacity induced within the photocoupler, input to an inversion input terminal of the operational amplifier 3 through the electrostatic capacity generated by the capacity formation pattern 13, so that the value of an output voltage to the common mode input signal can be made small, a common mode voltage rejection ratio (CMR) can be made good, and an output voltage error of the isolated amplifier can be made small.

COPYRIGHT: (C)1997,JPO

